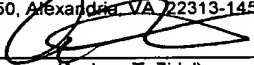




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USW 2154

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(Andrew T. Zidel)

Docket No.: SCEI 3.0-058
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Suzuoki et al.

Application No.: 09/816,752

Group Art Unit: 2154

Filed: March 22, 2001

Examiner: J. Chang

For: PROCESSING MODULES FOR COMPUTER
ARCHITECTURE FOR BROADBAND
NETWORKS

Commissioner for Patents
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INFORMATION DISCLOSURE STATEMENT

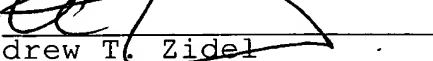
Dear Sir:

It is respectfully requested that the references listed on the enclosed form be made of record and considered with respect to the above-referenced U.S. patent application. Submission of the present Information Disclosure Statement should not be taken as an admission that the cited references are legally available prior art or that the same are pertinent or material.

In the event that any fee is due in connection with the present Information Disclosure Statement, the Commissioner is hereby authorized to charge the same to our Deposit Account No. 12-1095.

Dated: March 22, 2006

Respectfully submitted,

By 
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LD-458\



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				Application Number	09/816,752-Conf. #6544
				Filing Date	March 22, 2001
				First Named Inventor	Masakazu Suzuoki
				Art Unit	2154
				Examiner Name	J. Chang
Sheet	1	of	1	Attorney Docket Number	SCEI 3.0-058

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
	AA*	US-5,519,875	05-21-1996	Yokoyama et al.	
	AB*	US-6,192,514	02-20-2001	Lurndal	
	AC*	US-6,393,459	05-21-2002	Lurndal	
	AD*	US-6,421,736	07-16-2002	Breslau et al.	
	AE*	US-6,424,988	07-23-2002	Lurndal	
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	AI*	US-20020010807	01-24-2002	Multer et al.	
	AJ*	US-20020016863	02-07-2002	LURNDAL	

FOREIGN PATENT DOCUMENTS						
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